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CONFIRMATION NO. 4716

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10/734,300		349	2629	SON-1697/DIV

APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/466,969 12/20/1999 PAT 6,664,943

** FOREIGN APPLICATIONS *****

JAPAN P10-362283 12/21/1998
 JAPAN P11-023383 02/01/1999
 JAPAN P11-023384 02/01/1999
 JAPAN P11-023382 02/01/1999
 JAPAN P11-299188 10/21/1999

** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

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Foreign Priority claimed 35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
Verified and /PRABODH M DHARIA/ Acknowledged _____ Examiner's Signature	<input type="checkbox"/> Yes <input checked="" type="checkbox"/> No	Initials	JAPAN	31	30	5

ADDRESS

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TITLE

Digital/analog converter circuit, level shift circuit, shift register utilizing level shift circuit, sampling latch circuit, latch circuit and liquid crystal display device incorporating the same

FILING FEE RECEIVED 1122	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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